

Figure 1

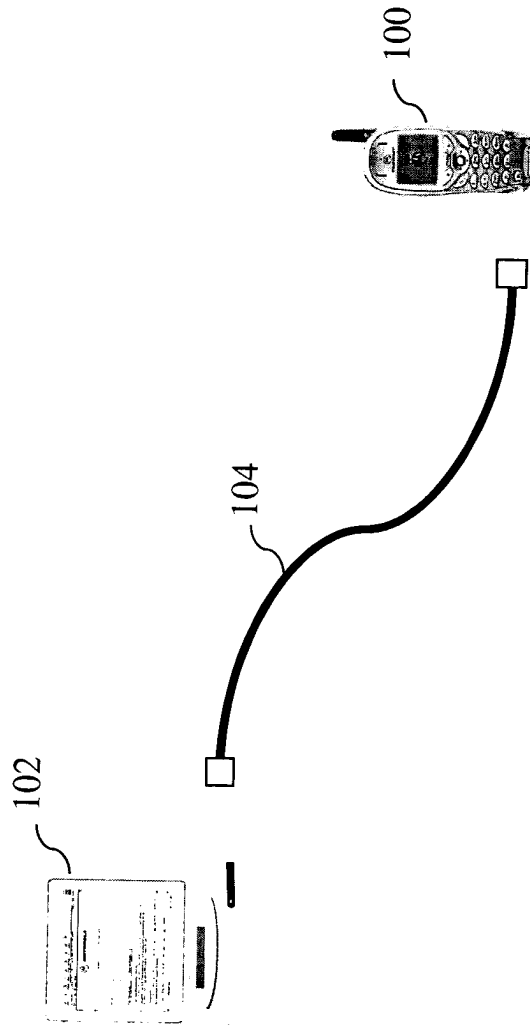
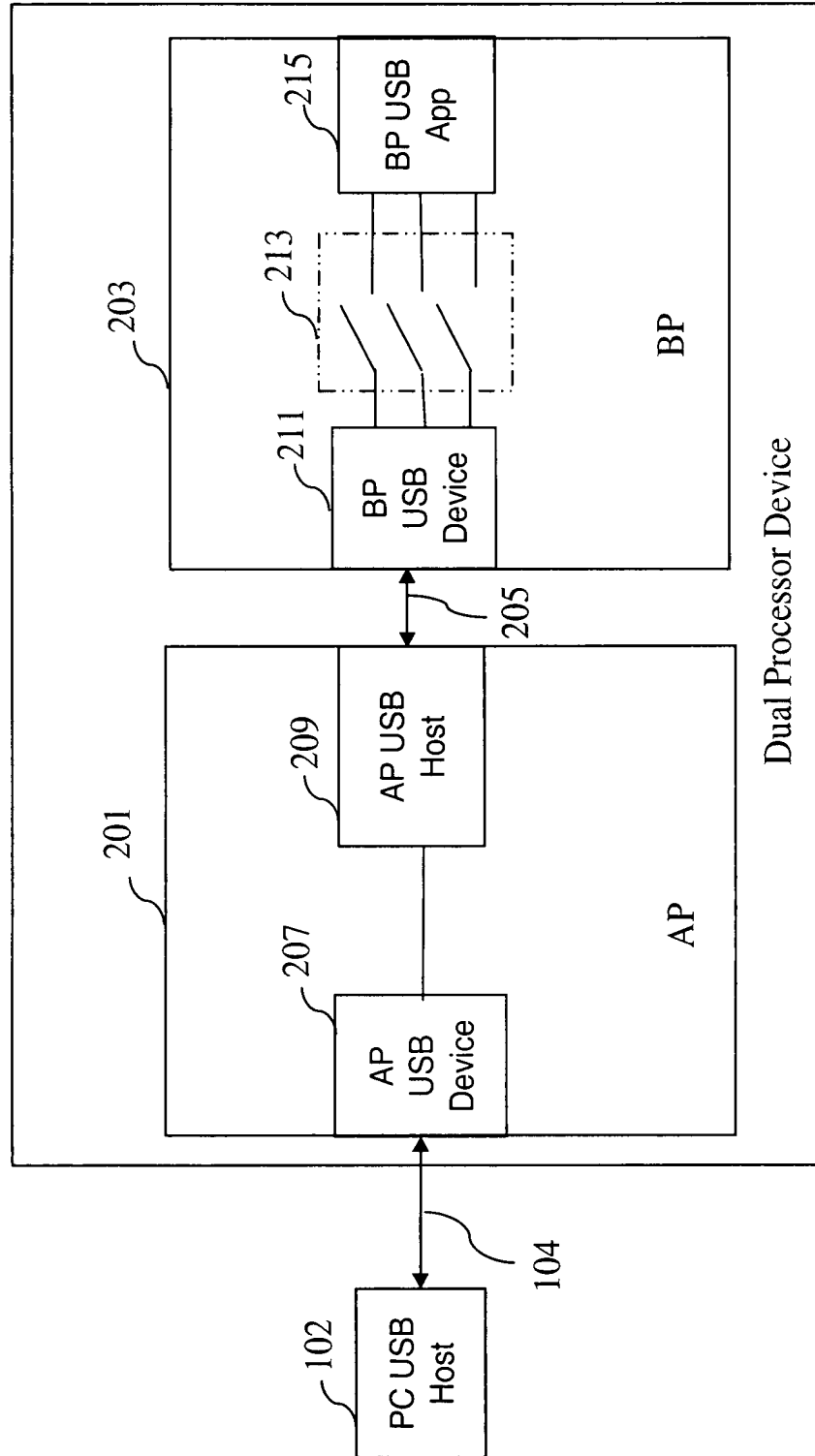


Figure 2
100



Dual Processor Architecture
- Enumeration Process and
Dynamic Service Switch
Operation at Power-Up

Figure 3

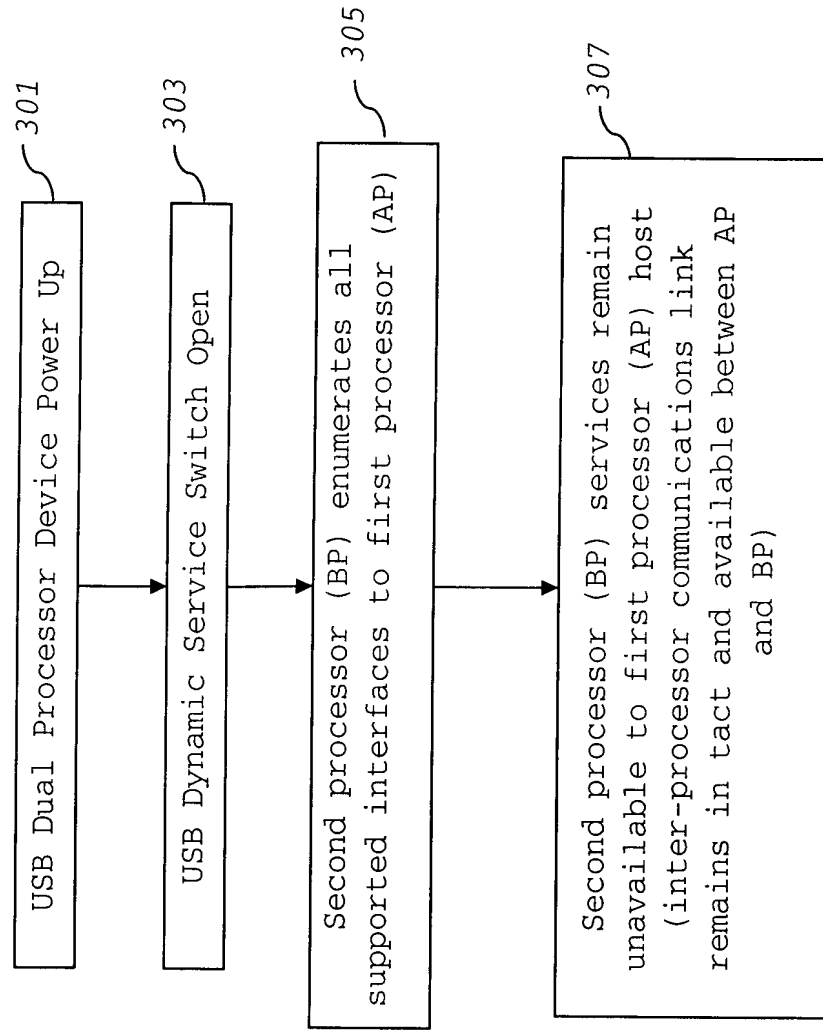
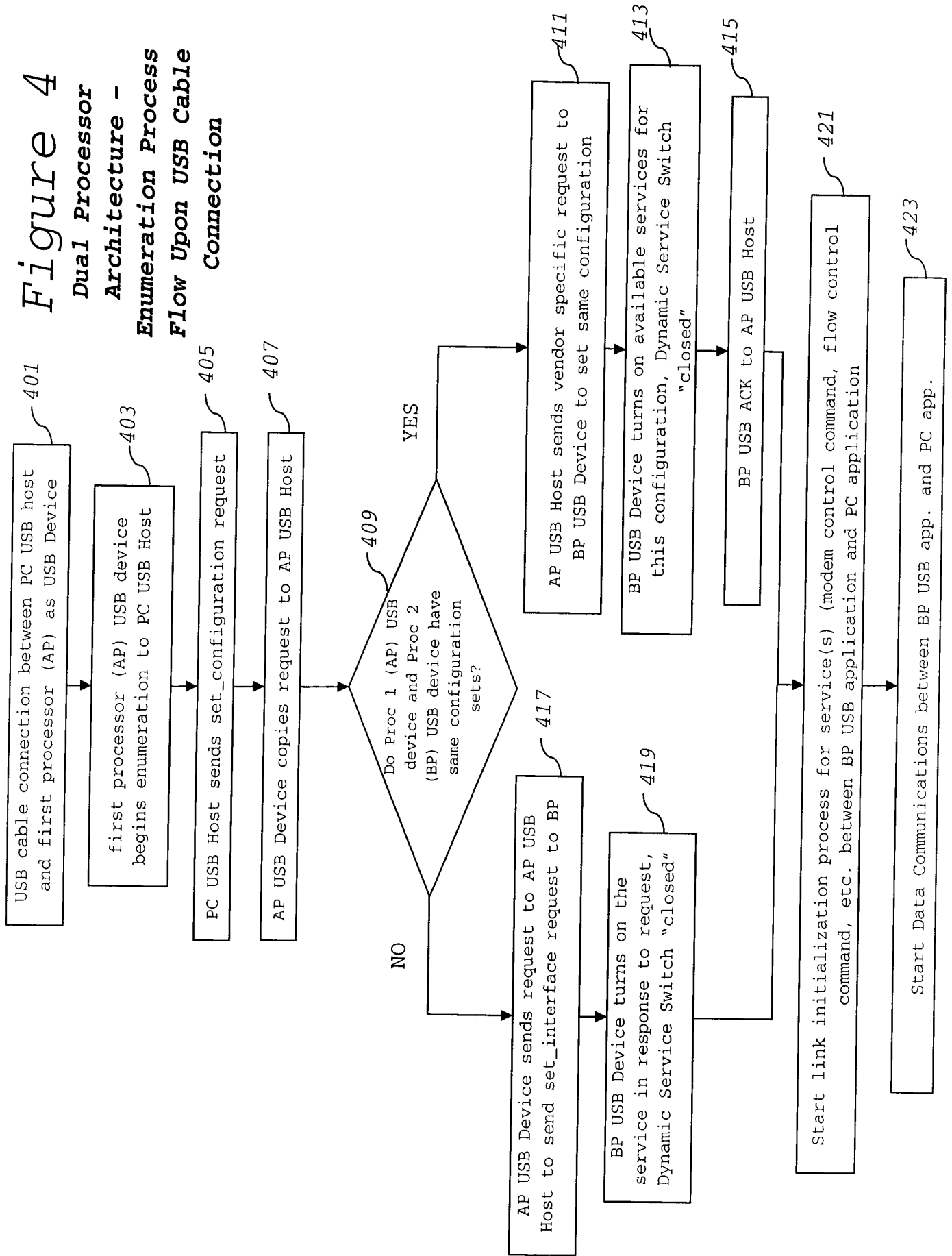


Figure 4

Dual Processor Architecture - Enumeration Process Flow Upon USB Cable Connection



**Dual Processor
Architecture - USB
Cable Disconnect
Process Flow**

Figure 5

